

## Amendments to the Specification

On page 18 of the specification, please replace paragraph [0051] with the following:

[0051] FIGs. 7A, 7B, and 7C illustrate several examples of the general structure of a 1/2 rate systematic and feedback type convolutional encoder used in the digital VSB transmission system according to the present invention. The convolutional encoder shown in FIG. 7A includes a first register *M2* 36 storing a previous register value, an adder 37 adding the value stored in *M2* 36 and the information bit *u*, a second register *M1* 38 storing the added value and outputting the stored value which is the parity bit.

On page 20 of the specification, please replace paragraph [0054] with the following:

[0054] When a supplemental data symbol bypasses the precoder of the trellis encoder used in the VSB system, the positive effect of the 1/2 rate convolutional encoding will be even greater. FIG. 8 illustrates a 1/2 convolutional encoder 51 and a trellis encoder 52 of the digital VSB transmission system. As it is shown in the figure, the supplemental data are bypassing the precoder. However, in order for the existing 8T-ATSC receiver to properly receive the MPEG data packets multiplexed with the supplemental data packets, data symbols other than the supplemental data symbol must be processed in the precoder. This problem can be solved using the modified precoder of the VSB transmission system shown in FIG. 9.